



JPW

IT0011-US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

<p>In re the Application of:</p> <p>MING-DOU KER, ET AL.</p> <p>Serial No.: 10/626,601</p> <p>Filed: July 25, 2003</p> <p>For: MIXED-VOLTAGE CMOS I/O BUFFER WITH THIN OXIDE DEVICE AND DYNAMIC N-WELL BIAS CIRCUIT</p>	<p>Art Unit: 2819</p> <p>Examiner: Not Yet Assigned</p>
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STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant respectfully inquires as to the status of the above-identified patent application,
and when the next communication from the United States Patent and Trademark Office
regarding this application can be expected.


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Date: September 23, 2004

Respectfully submitted,

MING-DOU KER, ET AL.

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